

REMARKS

At the outset, the Examiner is thanked for the thorough review and consideration of the subject application. The Office Action of June 9, 2004 has been received and contents carefully reviewed. Applicants gratefully acknowledge the Examiner's indication of allowable subject matter in claims 3-6 and 10.

By this Amendment, Applicants amend claims 3-5. Claims 2, 7-9 and 11-17 are withdrawn as the result of an earlier restriction requirement. Accordingly, claims 1-17 are currently pending in the present application. Reexamination and reconsideration of the application are respectfully requested.

Applicants acknowledge the Examiner's note regarding the inconsistency in the inventorship of the present application. As the declaration filed July 18, 2002 and the filing receipt of August 30, 2002, state, Jae Deok Park is the sole inventor of this application. However, the Applicants note that the rules regarding inventorship of a Korean application are not necessarily the same as the legal rights in a U.S. Patent application. However, the inventorship of the Korean priority document has been amended, and thus Applicants will submit a copy of the corrected Korean priority document in due course to address the Examiner's concerns.

The Examiner also objected to the drawings because the reference 2 in FIG. 1 is not described in the specification. In response, Applicants amended paragraph [0005] of the specification. No new matter is added to the specification. Accordingly, Applicants respectfully request the withdrawal of this objection.

The Examiner further objected to the drawings because the drawings do not show every feature of claims 3 and 4. With respect to the objection to claim 3, Applicants respectfully submit that in view of the amendments in claim 3, the objection is now believed to be moot. With respect to the objection to claim 4, Applicants have amended paragraph [0065] of the specification. FIG. 4 of the present application shows one of the blocks including first and second capacitors. No new matter is added to the specification. Accordingly, Applicants respectfully request the withdrawal of these objections.

Paragraph [0028] is also objected to because of informalities. Applicants respectfully submit that in view of the amendments in paragraph [0028], this objection is now believed to be moot. No new matter is added to the specification.

In addition, the Examiner rejected claims 3-6 and 10 under 35 U.S.C. § 112 ¶2, and rejected claim 1 under 35 U.S.C. § 102(b) as being anticipated by Takeda et al. (U.S. Patent No. 4,799,057).

With respect to the rejection of claims 3-6 and 10 under 35 U.S.C. § 112 ¶2, Applicants respectfully submit that in view of the amendments in claims 3 and 4, this rejection is now believe to be moot. Accordingly, claims 3-6 and 10 are now in full compliance with 35 U.S.C. § 112 ¶2.

The rejection of claim 1 under 35 U.S.C. § 102(b) as being anticipated by Takeda et al. is respectfully traversed and reconsideration is requested. Claim 1 is allowable over the cited references in that claim 1 recites a combination of elements including, for example, “a first start pulse input terminal for receiving one of a start pulse and an output signal of a previous block; and a second start pulse input terminal for receiving one of the start pulse and an output signal of a next block.” None of the cited references, singly or in combination, teaches or suggests at least this feature of the claimed invention.

In the Office Action, the Examiner cites Takeda et al. as teaching “a first start pulse input terminal for receiving one of a start pulse and an output signal of a previous block; and a second start pulse input terminal for receiving one of the start pulse and an output signal of a next block.” Applicants respectfully disagree.

In Takeda et al., each block of the sampling circuits (41a ~ 41d in FIG. 1A) is sequentially turned on in one direction (for example, left to right in FIG. 1A) according to either the control signals (Ea ~ Ed) as shown in FIGs. 1A and 1B or q_m and q_{m+1} as shown in FIGs. 6A and 6B. Different from Takeda et al., each block in the present application includes two input terminals: one for receiving one of a start pulse and an output signal of a previous block and the other for receiving one of the start pulse and an output signal of a next block, as claimed in claim 1. With the two input terminals, a shift register according to the present invention can drive the LCD panel bi-directionally. Accordingly, Applicants respectfully submit that claim 1 is

allowable over the cited references.

Applicants believe the foregoing amendments place the application in condition for allowance and early, favorable action is respectfully solicited. If the Examiner deems that a telephone conference would further the prosecution of this application, the Examiner is invited to call the undersigned attorney at the telephone number (202) 496-7500. All correspondence should continue to be sent to the below-listed address.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911.

Dated: November 9, 2004

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